

ABSTRACT

When there is created one composite image comprising an arrangement of a plurality of associated captured small images, an intended small image needs to be extracted from the composite image. When a plurality of small images are arranged to create one composite image, pixel data on the first row of a number-1 small image is stored at address adr 11 of DRAM. Pixel data on the second row and later are sequentially stored at specified addresses of DRAM. Likewise, number-2 through number-9 small images are stored at specified addresses of DRAM. In this manner, one composite image is created. When the number-1 small image is selected to be extracted, pixel data on the first row of the number-1 small image is read from address adr 11 of DRAM. Pixel data on the second row is read from address adr 21. Pixel data on the third row and later are sequentially read by incrementing addresses. The present invention can be applied to a digital still camera.